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I hereby certify that this paper is being deposited with the United States Postal Service with sufficient postage as First Class Mail under 37 CFR 1.8 on the date indicated below and is addressed to: "Commissioner of Patents and Trademarks, Washington, DC 20231."

Signed

Stuart T. Auvinen, Reg. No. 36,435

Date Signed and Mailed

4-9-01

PATENT  
LX-5  
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MAY 31 2001

Technology Center 2100

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Tseng

Serial No. 09/681,103

Filed: 1/5/01

For: Logic Emulator with Routing Chip providing  
Virtual Full-Crossbar Interconnect

Examiner:

Group Art Unit: 2825

TC 2800 MAIL ROOM

APR 13 2001

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INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks  
Washington, DC 20231

Sir:

Attached is a completed Substitute Form PTO-1449 and copies of the pertinent parts of the references cited thereon.

- ☐ The commissioner is hereby authorized to charge payment of any patent application processing fees under 37 C.F.R. § 1.17 associated with this communication or credit any overpayment to deposit account No. 01-2950.

Respectfully Submitted,

Stuart T. Auvinen  
Agent for Applicant  
Reg. No. 36,435